Docket No.: 67161-111 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Naoya WATANABE : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: November 26, 2003 : Examiner:

For: SEMICONDUCTOR MEMORY DEVICE HAVING EASILY REDESIGNED MEMORY

**CAPACITY** 

## INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the document listed on the attached form PTO-1449. It is respectfully requested that the document be expressly considered during the prosecution of this application, and that the document be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of Japanese Patent Laying-Open No. 3-80493, is discussed in the present specification.

## Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Facsimile: (202) 756-8087 **Date: November 26, 2003** 

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					APPLICANT Naoya WATANABE			
(PTO-1449)					FILING DATE November 26, 200	FILING DATE GROUP November 26, 2003		
				U.S. PATE	NT DOCUMENTS			
EXAMINER'S	Document Number Publication Date				Name of Patentee or Applicant of Cited		Pages, Columns, Lines, Where	
INITIALS	CITE NO.	Number-Kind Code2 (# known)		MM-DD-YYYY	Document Document		Relevant Passages or Relevant Figures Appear	
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.